

**Amendments to the Specification**

Please amend the following paragraph that begins on page 7, line 13 as follows:

The processing circuit 56 may include a digital signal processor or a high speed microprocessor, or a combination of these and/or other processing devise. Many processing circuits that can generate a variety of electricity metering information from digital voltage and current measurement signals are known in the art, and include those shown in U.S. Patent No. ~~6,121,158~~ 6,112,158 to Bond et al., and U.S. Patent No. 5,544,089 to Hemminger et al., both of which are incorporated herein by reference.

Please amend the following paragraph that begins on page 8 line 1 as follows:

Fig 2, shows in further detail an exemplary embodiment of a first current sensor 16a of the current sensors 16 of Fig. 1, and an A/D converter 41 of the data acquisition subsystem 18. The first current sensor 16a is configured to obtain a phase A voltage measurement signal. It will be appreciated that in some embodiments, the data acquisition subsystem 18 may include a multiplexer or other switch interposed between the current sensor 16a and the A/D converter. The multiplexer does not affect the operation of the current sensor 16a, but allows the A/D converter 41 to sample multiple signals, including signals other than the one provided by the first current sensor 16a.

~~U.S. Patent No. (1505 0007) shows this type of arrangement.~~